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| U.S. Department of Commerce, Patent and Trademark Office | | | | | Atty Docket No. | | Serial No. | |
| | | | | | M-15260 US | | 10/718,501 | |
| INFORMATION DISCLOSURE STATEMENT BY APPLICANT | | | | | Applicant(s) | | | |
| (Use several sheets if necessary) | | | | | Jishnu Bhattacharjee et al. | | | |
| | | | | | Filing Date | | Group | |
| | | | | | November 19, 2003 | | Unassigned | |
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| *Examiner Initial | | Document Number | Date | Name | Class | Subclass | Filing Date If Appropriate | |
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| | | | | | | | Translation | |
| | | Document | Date | Country | Class | Subclass | Yes | No |
| | AG | | | | | | | |
| | AH | | | | | | | |
| OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.) | | | | | | | | |
| AI | | H.Y. D. Yang "Design Considerations of Differential Inductors in CMOS Technology", Dept. of ECE, University of Illinois at Chicago. | | | | | | |
| AJ | | | | | | | | |
| Examiner | | Date Considered 7/26/04 | | | | | | |
| *EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant. | | | | | | | | |